

David York

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Summary of Qualifications

Seasoned professional with 35-years of laboratory operations, technology/patent analysis, claim chart preparation/analysis and technical writing experience supporting quality control, litigation, licensing, arbitration, investment, competitive intelligence and technical documentation projects.

Professional Experience and Accomplishments

Patent Analyst, Subject Matter Expert, Technical Writer, Chip Patents LLC: Columbus, OH; 2011 - present

- Provide subject matter expertise in the areas of semiconductor device structures and manufacturing processes.
- Develop illustrative claim charts to support licensing, infringement, monetization, and procurement activities.
- Identify current and potential future product/market applications for patented inventions.
- Analyze claim charts and compare to patent elements to evaluate applicability and technical strength of evidence.
- Conduct detailed technical patent analysis to support licensing, procurement, valuation and sales.

Patent Analyst and Subject Matter Expert, IC Microanalysis LLC: Phoenix, AZ; 2004 – 2011

- Performed technical patent portfolio analysis to quickly understand, categorize, and summarize unique features.
- Located relevant patent and non-patent literature using various search strategies and software tools.
- Served as a technical expert on an arbitration panel overseeing a contract dispute involving patent licensing.
- Heard oral arguments and testimony at hearings, reviewed motions, and provided impartial analysis and decisions on discovery disputes, claim construction, validity, infringement, and contract interpretation.

Manager, Process Analysis, Chipworks, USA (Acquired ICE): Scottsdale, AZ; 2001 - 2003

- Independently directed and coordinated all U.S. - based operations of analytical services department.
- Ensured smooth transition to and compliance with new corporate operating procedures by motivating staff.
- Minimized duplication of effort by integrating key department operating functions with new corporate office.
- Maintained high product quality and customer service throughout 9-month transition of U.S. operations to Canada.

Vice President, Intellectual Property Services - Integrated Circuit Engineering: Scottsdale, AZ; 1998 – 2000

- Supported a wide range of competitive, quality, licensing, and patent analysis projects and programs.
- Developed a focused services division to support a quickly expanding intellectual property client base.
- Provided expert testimony in multiple infringement cases in support of laboratory evidence.
- Assisted with archiving of reports for inclusion in Smithsonian Chip History Exhibit <http://smithsonianchips.si.edu/>.

Manager, Laboratory Services - Integrated Circuit Engineering: Scottsdale, AZ; 1984 – 1998

- Provided operations and technical management of \$3 million analytical services department employing 12 analysts with annual sales of \$2.5 million. Actively performed laboratory analysis of products in addition to managerial duties.
- Directed laboratory analysis of real-world semiconductor products in support of evidence of use investigations.
- Responded to changing client priorities and evolving market conditions by re-inventing products and processes.
- Reduced costs and increased profitability by developing processes that maximized usage of existing equipment.

Laboratory Analyst/Engineer - Integrated Circuit Engineering: Scottsdale, AZ; 1979 – 1984

- Performed laboratory analysis on a wide range of products in support of competitive and patent investigations.
- Decapsulated samples, selectively removed process layers, cross-sectioned die and packages, wrote technical reports.
- Reviewed patents to determine analysis required to show patented feature.

Deposition Experience

Deposed as a technical expert in support of laboratory results in the following cases:

- *Atari vs. Nintendo*, 1992 - Re: 10NES ROM source code.
- *Neomagic vs. Trident*, 1999 - Re: Circuit substrate power biasing.
- *Ericsson vs. Harris*, 2000 - Re: Fabrication process-telephone power circuit.

- Provided written Witness Statements in support of Integrated Circuit Engineering Corporation public SCA/SUB Reports for disputes before the International Trade Commission (ITC).

Publications

- *Successful Technologies Review, 2001* - Summary and comparison of new integrated circuit process technologies. Published in 2001 by Chipworks.
- *Interlevel Dielectric (ILD) and Metallization Technologies Focused Technology Review*. Published in 2001 by Chipworks.
- Wrote thousands of claim charts, competitive product analysis reports, quality construction analysis reports, infringement analysis reports, and reverse engineering reports on a wide range of products while at ICE Corporation and Chipworks. Published 1979 - 2003.

Education and Specialized Training

- Bachelors degree, Technical Communications; Arizona State University. Graduated summa cum laude.
- Associates degree, Electronics Engineering; Stark State College. Graduated with high distinction.
- EAST and WEST Search Tool Training - US Patent and Trademark Office, Alexandria, VA
- Patent Review and Classification - Chipworks USA
- State Bar of Arizona's American Arbitration Association (AAA) Private Arbitration Update Seminar - 2009
- Certificate of Completion – Introduction to Dreamweaver CS3
- Design of Experiments - ICE Corporation
- Integrated Circuit Fabrication and Status of the Industry - ICE Corporation
- Regularly attend various industry technical seminars and conferences - SEMICON, IRPS, IEDM

Specialized Technical Experience

- **Analytical Techniques:** Chemical and mechanical decapsulation, optical inspection and imaging, chemical and plasma-based delayering, selective package and die metallurgical cross-sectioning, chemical feature enhancement, feature size and layer thickness measurements, FESEM sample preparation, high-resolution FESEM inspection and imaging, process and circuit reverse engineering.
- **Products:** Integrated Circuits (Memories, Microprocessors, Microcontrollers, Interface Products, Power FETs, Opto-isolators, Opto-couplers, Transistors), Packages, Pressure Transducers, Displays, Memory Cards, Hybrids, Passives (Chip Diodes, Chip Inductors, Chip Capacitors, Connectors), MEMS, Printed Circuit Boards, Inkjet Cartridges.

Technical Communications Experience

- **Documentation:** Technical reports, operating procedures, claim charts, video/audio production/editing, multi-media PowerPoint. Website content development/search engine optimization/editing/usability, marketing brochures, flow charts. User instructions, quick reference cards, online help documents, screen-casts, proposals.
- **Software/Web Applications:** MS Office (Word, Excel, PowerPoint, Publisher, and Explorer), LinkedIn, YouTube. Acrobat, Photoshop, Visio, Namo Web Editor, Vegas Video, Sound Forge Audio, Snagit, Survey Monkey. Adobe InDesign and Illustrator, Camtasia Studios, Help-n-Doc, Dreamweaver, Blackboard, E-Course, HTML.